EOA: Gate output that emits a positive voltage as soon as the attack phase has ended. The gate signal ends at the start of the release phase.

EOR: Gate output that emits a positive voltage once the release phase is over. The gate signal ends at the end of the attack phase.

Inv. Out: Inverted output for the envelope voltage - without an intervening VCA (i.e. fixed minimum value at the end of the attack phase of approx. -7V).

Var. Out: Normal (positive) output of the envelope voltage after the VCA. A control voltage of approx. 5V at the input "Lev. CV" leads to an amplification of 1 (i.e. maximum voltage of +7V). Higher control voltages lead up to a maximum of +12V, after which clipping sets in (i.e. the voltage curve is simply cut off at 12V).

Fixed Out: Normal (positive) output of the envelope voltage without an intervening VCA (i.e. fixed maximum value at the end of the attack phase of approx. +7V).