Analog Sum: Output for the sum of the attenuated signal from "+In", the attenuated and inverted signal from "-In" and the offset voltage (which can be positive or negative).

Inv. Cmp. Out: Inverted gate signal (i.e. positive when "Cmp. Out" = 0 V and 0 when "Cmp. Out" is positive).

Cmp. Out: Gate output that emits a positive voltage as soon as the internal sum (see "Analog Sum" above) is greater than 0 V. The gate signal is 0V when the internal sum is less than or equal to 0V.