

**Gate:** A voltage present here starts the envelope. As long as the gate signal persists, the attack and then the decay phase are run through until the output voltage remains at the sustain level. As soon as the gate signal breaks off, the release phase is started - regardless of whether we were previously in the attack or decay phase or whether the voltage was at the sustain level.

**Retrig.:** A trigger signal at this socket restarts the envelope - but only when the attack phase has already ended and as long as the gate signal is still present, otherwise the retrigger is ignored.

